



**POTTI SRIRAMULU CHALAVADI MALLIKHARJUNA RAO  
COLLEGE OF ENGINEERING & TECHNOLOGY**

Sponsored by: SKPVV Hindu High School Committee, Estd:  
1906 (Approved by AICTE Affiliated to JNTU Kakinada)

ISO 9001:2015 Certified

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**FACULTY PROFILE**

**B.Praveen Kitti**

S/o B.Koteswara Rao

D.No : 7-506 C,

Tipparla Bazar, Mangalagiri

Andhra Pradesh

Mobile: 9959949311

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**Experience:**

Teaching: 7 Years

Industry: Worked as an Intern in “**Vedic School of VLSI Design**” a Training Division of **Simpli5NG Semiconductors** from August 2010 to May 2011.

**Career Profile**

Year	Organization	Role
2012 – Till Date	Potti Sriramulu Chalavadi Mallikharjuna Rao college of Engineering And Technology, Vijayawada	Assistant Professor

## **Qualification**

<b>Degree and Date</b>	<b>Board / University</b>	<b>Date of Completion</b>	<b>% Marks</b>
Pursuing Phd (Antennas)	K L University	--	--
M.S ( VLSI )	Manipal University	June 2011	7.7 GPA
B.Tech (ECE)	Nagarjuna University	April 2008	62.72
MPC	IPE	2004	84.10
X <sup>th</sup>	SSC	2002	76.50

## **The subjects handled:**

Electromagnetic Waves and Transmission Lines,  
Antennas and Wave Propagation,  
Microwave Engineering,  
Switching Theory and Logic Design,  
Signals & Systems

## **Certifications**

<b>Certification</b>	<b>Date Certified</b>
Introduction to Electromagnetism (NPTEL)	April 2015

## **Affiliation to Professional Bodies**

ISTE - LM90562

IAENG - 189511

ISSE

## **Publications**

- Published a paper titled “Design of low power SAR ADC with two different DAC structure and two different SAR logic designs and their comparisons” held at VIT, Vellore, Tamilnadu in December 2018.
- Published a paper in SPRINGER conference titled “Design of Microstrip Antenna with Extended Ground” held at UPES, Dehradun, Uttarakhand in December 2018.
- Published a paper in SPRINGER conference titled “Electroencephalogram-Based Open BCI Devices For Disabled People” held at Raghu Educational Society, Vishakhapatnam in January 2017.
- Published a paper in SPRINGER conference titled “Line based Successive cancellation decoder with reduced complexity” held at UPES, Dehradun, Uttarakhand in April 2017.
- Published a paper in IJERT conference titled “Design of Miniature Wideband Microstrip Patch Antenna”, with ISSN 2278-0181, Volume 4, Issue 18, May 2016.
- Published a paper in IJERT conference titled “Design of Planar Inverted-F Antenna for WIMAX/RAS Application”, with ISSN 2278-0181, Volume 4, Issue 18, May 2016.
- Published a paper in IJERT conference titled “Dual band Notch Antenna for UWB Applications”, with ISSN 2278-0181, Volume 4, Issue 18, May 2016.
- Published a paper in IJERT conference titled “Design of Planar Monopole Microstrip Antenna”, with ISSN 2278-0181, Volume 4, Issue 18, May 2016.
- Published a paper in IJERT conference titled “A Simple Planar Rectangular Antenna with Band Distribution”, with ISSN 2278-0181, Volume 4, Issue 18, May 2016.
- Published a paper in IJERT conference titled “Design of Circular Patch Antenna for UWB Applications”, with ISSN 2278-0181, Volume 4, Issue 18, May 2016.
- Published a paper in IJAER titled “I2C Master Bus Controller Implementation on FPGA with RAM as Slave” with ISSN 0973-4562 Volume 10, Number 10 (2015) pp. 25563-25570.

## **Conferences, Seminars, Workshops & Guest Lectures Attended**

- Attended a one week workshop an ISTE-SRM – Short Term Training Program (STTP) held at Sri Vasavi College of Engineering on “Trends in Antenna design & Applications”.
- Attended a one week workshop on “Design and Simulation of Antennas & Microwave Devices using ANSYS HFSS held in SVECW, Bhimavaram.
- Attended a one week workshop on “RF Antenna Design using ANSYS HFSS held in LBRCE, Mylavaram.
- Attended a two day IEEE conference on Radio Frequency, Antenna Applications and Measurements Symposium - RAMS 2014 held at SRM University - Chennai.

- Attended a one week workshop on “Research Methodology held” at KL University – Vaddeswaram.
- Attended a one week workshop on “Speech signal Processing” held at NIT – Warangal.
- Attended a Two week ISTE workshop conducted by IIT Kharagpur on “Analog Electronics”
- Attended a Two week ISTE workshop conducted by IIT Kharagpur on “Signals & Systems”
- Attended a Two week QIP FDP conducted by IIT Kharagpur on “Efficient & Low-Loss Antenna Configurations”
- Attended a one day workshop titled “5G Technology Knowledge and Skills” on 24<sup>th</sup> August 2018.
- Attended a one week FDP on “Advances in Microwave Antenna Design” , organized by E & ICT academy, NIT Warangal.

### **Events organized/ Conducted**

- Organized a one week workshop on “Hands on experience with HFSS” , in PSCMRCET
- Acted as a resource person for a two day workshop titled “Antenna Design using HFSS”, in VSM college of Engineering, Ramachandrapuram.

### **Funding Projects Handled (If any – Please provide the details of the project)**

**Praveen Kitti**  
**Signature**